

Product Details

The SDP-I-FMC enables easy connection of a wide variety of FPGA evaluation boards to the range of daughter boards from ADI's System Demonstration Platform (SDP) family. The SDP-I-FMC has a Low Pin Count (LPC) FMC connector and a 120 pin connector to connect to SDP daughter boards. The SDP-I-FMC can only be used with 3.3VIO FPGA evaluation boards.

Example projects for prototyping with an FPGA evaluation board, the SDP-I-FMC, and an SDP daughter evaluation board can be found on the ADI [wiki](#).

The System Demonstration Platform is a collection of controller boards, interposer boards, and daughter boards, used for easy, low cost evaluation of ADI components and reference circuits. For a general overview of the entire platform visit the System Demonstration Platform homepage, www.analog.com/sdp

SDP-I-FMC



Documentation

- [SDP-I-FMC Schematic](#) (pdf)
- [SDP Wiki](#) (Wiki Site)
- [SDP Facilitates Quick Prototyping and Evaluation](#)
Read our Analog Dialog Article for more information on the SDP
- [System Demonstration Platform Simplifies Evaluation](#)
Learn more about the SDP with the SDP video

Software and Tools

- [Example Project for SDP daughter boards on ADI wiki](#) (Wiki Site)



PRICE, PACKAGING, AVAILABILITY

SDP-I-FMC Interposer

[Print Table](#)

Model	Description	Price	RoHS	View PCN/ PDN	Check Inventory/ Purchase/Sample
SDP-FMC-IB1Z Status: Production	SDP to FMC Interposer Board	\$	Yes	-	☐